Matrix Approach to Deadlock-Free Dispatching in Multi-Class Finite Buffer Flowlines

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Abstract. For finite-buffer manufacturing systems, the major stability issue is 'deadlock', rather than 'bounded-buffer-length stability.' The paper introduces the concept of 'system deadlock,' defined rigorously in Petri net terms, and system operation with uninterrupted part-flow is characterised in terms of the absence of this condition. For a large class of finite-buffer multiclass re-entrant flowline systems an analysis of 'circular waits' yields necessary and sufficient conditions for the occurrence of 'system deadlock'. This allows the formulation of a maximally permissive one-step-look-ahead deadlock-avoidance control policy for dispatching jobs, while maximizing the percent utilisation of resources. The result is a generalized kanban dispatching strategy, which is more general than the standard multi-class last buffer first serve (LBFS) dispatching strategies for finite buffer flowlines that typically under-utilize the resources. The problem of computational complexity associated with Petri net applications is overcome by using certain sub-matrices of the PN incidence matrix. Computationally efficient matrix techniques are given for implementing the deadlock-free dispatching policy.

1. Introduction

In *flexible manufacturing systems* (FMS) [2] resource sharing is ubiquitous: a given resource may be common to the production processes of several part-types (*parallel sharing*), and/or may be used multiple times during the production process of a given part-type (*sequential sharing* or *reentrance*). A key role in job routing/dispatching is played by the FMS *controller*, which allocates resources to perform jobs for customers or on parts. Failure by the controller to assign resources suitably during job dispatching can lead to serious performance problems. There are numerous formal job-dispatching rules, such as first-in-first-out (FIFO), first-buffer-first-serve (FBFS), last-buffer-first-serve (LBFS), earliest due date (EDD), least slack (LS), and so on [11], [13].

One fundamental question that needs to be addressed in connection with any FMS dispatching policy is whether it is *stable*. Studies of stability for FMS often focus on stability in the sense of *bounded buffer lengths* [9], [11]. However, in practice the buffer lengths are *finite*, and such stability results are inapplicable, since it is not obvious how to keep the buffer lengths below some *fixed finite value*. For finite-buffer multi-class reentrant flowline (MRF) systems [9], which constitute a large class of FMSs, the issue is stability, not in the sense of bounded buffer lengths, but in the sense of absence of *deadlock*. A flowline for a given part-class is said to be deadlocked if it holds a part that cannot complete its

processing sequence. Many popular dispatching rules can result in deadlock if care is not taken (for instance, see [14]). In [11] the FBFS and LBFS policies have been shown to be stable for single-part flowlines with no buffer limits; however, both policies can cause deadlock when the buffer lengths are finite. In a finite-buffer system, any dispatching policy for uninterrupted part flow essentially has to take into account the *structure* of the interaction between jobs and resources. Several results based on such a structural approach may be discovered in [1], [4], [7], [8], [10], and [15]. In all of these but [7] *Petri net* (PN) formalism is used for system modeling.

This paper focuses on MRF systems without *assembly*. Based on a PN model, flowline deadlock is characterised in terms of *part-path deadlock*, and using this notion the more general condition of *system deadlock* is defined. By exploiting the structure of the job/resource interactions, it is shown that for a large class of systems, called the *regular* MRF systems, the two types of deadlock are equivalent to a condition called *circular blocking* (CB) [10]. The analysis not only generalizes the results of [3] and [15] to a much larger class of systems, but also offers a rigorous structural framework in which the problem of 'impending part flow deadlocks' mentioned in these references can be handled.

The paper also provides dynamic job-dispatching rules driven by a one-step-look-ahead deadlockavoidance control policy. A generalized multi-part LBFS job dispatching policy is defined and shown to be deadlock-free. The proposed control scheme is maximally permissive [5] and can be viewed as a sort of generalized kanban job-dispatching strategy, including the special LBFS policies of [11]. Matrix techniques, based on certain sub-matrices of the PN incidence matrix, which come directly from industrial engineering methods, are used to obtain analytic computational formulae for deadlock analysis and avoidance. This deals with the well-known problem of complexity arising from PN applications.

2. MULTIPLE REENTRANT FLOWLINE SYSTEM MODEL

Let Π be the set of distinct types of parts produced (or customers served) by an MRF system, where each part type $k \in \Pi$ is characterized by a predetermined sequence of jobs $J^k = \{J_1^k, J_2^k, ..., J_{L_k}^k\}$, with at least one resource used for each job. Let R denote the set of system resources, with each $r \in R$ a pool of multiple copies of a given resource. Since there is no assembly involved, one can uniquely associate with each job sequence J^k the operations of raw part-in, J^k_{in} , and finished product-out, J^k_{out} .

The MRF system activity is described by a PN model, called the *flowline system PN* (FPN), defined as follows. Let N = (P, T, I, O) denote the FPN, where I represents the input arcs to T from P and O represents the output arcs from T to P. Given any node $v \in P \cup T$, let $v \in P \cup T$. the pre-set and post-set of v in the usual way, i.e., the set of nodes that have arcs to and from $v_{\rm v}$, respectively. For a set of nodes $V = \{v_i\}$, define $V = \{v_i\}$ and $V = \{v_i\}$. Define $P = R \cup J \cup J_{in} \cup J_{out}$, with R, J_{in} , and J_{out} as the sets of places respectively representing the availability of resources, part arrivals and finished products, and J as the set of places representing the ongoing jobs. The set of transitions T can be partitioned as $T = \bigcup_{k \in \Pi} T^k$, where $T^k = \{t_1^k, t_2^k, \dots, t_{L_k+1}^k\}$, with $\mathbf{t}_{i}^{k} = {}^{\bullet}\mathbf{J}_{i}^{k} = \mathbf{J}_{i-1}^{k}$, for $i \notin \{1, L_{k}\}$; while $\mathbf{t}_{1}^{k} = {}^{\bullet}\mathbf{J}_{1}^{k} = \mathbf{J}_{in}^{k} {}^{\bullet}$ and $\mathbf{t}_{L_{k}+1}^{k} = \mathbf{J}_{L_{k}}^{k} {}^{\bullet} = {}^{\bullet}\mathbf{J}_{out}^{k}$. In other words, each transition corresponds to a *decision* or *rule* for the starting and/or completion of a job, which also involves the allocation and/or release of at least one resource. Thus $J^k \cup T^k$ defines a distinct *part-path*, with initial place $J_{in}^{k} = {}^{\bullet}t_{1}^{k}$, with ${}^{\bullet}J_{in}^{k} = \emptyset$, and terminal place $J_{out}^{k} = t_{L_{k}+1}^{k} {}^{\bullet}$, with $J_{out}^{k} {}^{\bullet} = \emptyset$. Resource places always occur off part-paths. For any $r \in R$, define the *job set* J(r) as the set of jobs using r, and resource loop $L(r) = r \cup J(r)$. Given a set of resources $Q \subset R$, define the job set of Q as $J(Q) = \bigcup_{r \in Q} J(r)$. Denote by $R(J_i^k)$ the resources used by job J_i^k .

An available resource or an ongoing job is indicated by *tokens* in the respective places. It is assumed that places in J_{in} are always marked (i.e., there is always a part ready to enter) and those in J_{out} are always empty (i.e., finished product is pulled out immediately). The *marking* $m : P \rightarrow Z$, with Z as the set of nonnegative integers, gives the distribution of tokens. $\{N, m\}$ denotes the *marked* FPN. The *initial marking* m_0 represents the *idle state* (i.e., no parts in process), and $\Re(m_0)$ shows the set of all markings reachable from m_0 . For a set of places S, the set marking is defined as $m(S) = \sum_{p \in S} m(p)$. A transition $t \in T$ is said to be *dead* at *m* if there exists no $m' \in \Re(m)$ that enables it, with $\Re(m)$ as the set of markings reachable from *m*. t is said to be *job(resource)-enabled* if $m({}^{\bullet}t \cap J) > 0$ ($m({}^{\bullet}t \cap R) > 0$). A marking *m* is said to be dead if no $t \in T$ is enabled at *m*. A place $p \in P$ is said to be dead at *m* if m(p) = 0 = m'(p) for all $m' \in \Re(m)$.

In a PN, a siphon (trap) is a set of places $S \subset P$ ($Q \subset P$) such that ${}^{\bullet}S \subset S^{\bullet}$ ($Q^{\bullet} \subset {}^{\bullet}Q$). A key feature of a siphon (trap) is that, once its marking becomes zero (nonzero) it will never again become nonzero (zero). Thus if a siphon contains a marked trap, it will never become empty. A siphon (trap) is said to be *minimal* if it does not contain any other siphons (traps). It is easy to see that every resource loop L(r) is both a minimal siphon and a minimal trap and its total token load is conserved at all $m' \in \Re(m_0)$.

Subsequent analysis in this paper deals with the class of MRF systems specified below. This class is more general than the ones in [1], [3], [5] and [15]: it allows resource pools as well as generalized buffering strategies including one buffer per machine, shared buffers or machines without buffers. **Definition 1:** [Class of MRF₁ systems] Define MRF₁ as the class of MRF systems with the FPN description N satisfying the following: (i) $\forall p \in P$, ${}^{\bullet}p \cap p^{\bullet} \neq \emptyset$; (ii) $\forall k \in \Pi$, $t_1^{k^{\bullet}} \cap P \setminus J = \emptyset$ and ${}^{\bullet}t_{L_k+1}^k \cap P \setminus J = \emptyset$; (iii) $\forall J_i^k \in J$, $|R(J_i^k)|=1$ and $R(J_i^k) \neq R(J_{i+1}^k)$; (iv) $\forall J_i^k \in J$, $|J_i^{k^{\bullet}}|=1$; (v) $\forall t_i^k \in J$,

 $|{}^{\bullet}\mathbf{t}_i^k \cap \mathbf{J} \mid \leq 1; \, (\mathrm{vi}) \ \forall \mathbf{r} \in \mathbf{R} \,, \, | \, \mathbf{J}(\mathbf{r}) \mid \geq 1.$

This means that there are no self loops, each part-path has a well-defined beginning and an end, every job requires one and only one resource with no two consequent jobs using the same resource, there are no choice jobs and no assembly jobs, and there are shared resources. Obviously in MRF₁ systems, for any $r \in R$, $J(r) = r^{\bullet \bullet} \cap J = {}^{\bullet \bullet} r \cap J$ and $R(J_i^k) = {}^{\bullet \bullet} J_i^k \cap R = J_i^k {}^{\bullet \bullet} \cap R$.

3. Stability For MRF₁ Systems — Deadlock

For finite-buffer systems, any stability analysis must address the issue of deadlock avoidance so that no part in the flowline is held up, that is every part in the flowline must come out as a finished product. The following two definitions provide a rigorous characterisation of this condition. **Definition 2:** [Part-path deadlock] Given a system of class MRF₁ with $\{N, m_0\}$, part-path $k \in \Pi$, is said to be deadlocked at any $m \in \Re(m_0)$ if some job-enabled transition t_i^k is dead at m.

For any part-path $k \in \Pi$, let $\overline{J}^k \subset J^k$ denote a nontrivial subset of consecutive jobs. Define $M_{\overline{J}^k}^m \subset \Re(m)$ to be the subset of markings reachable from m via a sequence of markings $\{m^i\}$ such that $m^i(\overline{J}^k) \leq m(\overline{J}^k)$, for all i (i.e., no new part goes into \overline{J}^k). An uninterrupted flow of parts clearly means that every part in any \overline{J}^k can move through the rest of its job sequence without deadlocking any part-path. This requires absence of deadlock for the entire system in the sense specified below.

Definition 3: [System deadlock] Given a system of class MRF₁ with $\{N, m_0\}$, N is said to be deadlocked at any $m \in \Re(m_0)$ if either (a) some part-path is deadlocked at m, or (b) there exists some $\overline{J}^k \subset J^k$ with $m(\overline{J}^k) > 0$, such that at every $m^i \in M_{\overline{J}^k}^m$ with $m^i (\overline{J}^k)=0$, some part-path is deadlocked.

Consider now a dispatching policy U for $\{N, m_0\}$, and let $M_U \subset \Re(m_0)$ denote the set of markings reachable under U. Note that U, by controlling the firing of certain transitions of N, limits the set of reachable markings to M_U . Such a policy is said to be maximally permissive if M_U is maximal with respect to certain imposed specifications [5], e.g., deadlock-freeness. A deadlock-free dispatching policy can be defined as follows.

Definition 4: [Deadlock-free dispatching policy] A dispatching policy U is said to be deadlock-free if N is not deadlocked at any $m \in M_U$.

It is shown in [10] that part-path deadlock is equivalent to a system condition called *circular blocking* (CB), which is a consequence of the existence of *circular wait relations*. System deadlock, on the other hand, specifies a condition in which, although there may be no CB, further dispatching of jobs will inevitably result in one (cf. 'impending part flow deadlock' in [3], and 'cyclic deadlock structure chain with key resource' in [15]). Thus, avoiding CB is necessary but generally not sufficient for a deadlock-free dispatching policy.

The notions of circular wait and circular blocking are recalled next (see [10] for further detail). For any two $\mathbf{r}_i, \mathbf{r}_j \in \mathbf{R}$, \mathbf{r}_i is said to *wait* for \mathbf{r}_j , denoted $\mathbf{r}_i \rightarrow \mathbf{r}_j$, if the availability of \mathbf{r}_j is an immediate requirement for the release of \mathbf{r}_i , i.e., if $\mathbf{r}_i \cap \mathbf{r}_j \mathbf{e} \emptyset$. This binary relation on \mathbf{R} is referred to as a *wait relation* and can be captured by the *wait relation graph* $\mathbf{G}_w = (\mathbf{R}, \mathbf{A})$, a digraph where \mathbf{R} is the set of nodes, and $\mathbf{A} = \{\mathbf{a}_{ij}\}$ is the set of edges, with \mathbf{a}_{ij} drawn if $\mathbf{r}_i \rightarrow \mathbf{r}_j$. In \mathbf{G}_w , $\mathbf{r}_{i_1} \rightarrow \mathbf{r}_{i_2} \rightarrow \dots \rightarrow \mathbf{r}_{i_w}$ defines an *Rpath* $\mathbf{r}_{i_1} \rightarrow \mathbf{r}_{i_w}$. Any set $\mathbf{C} \subset \mathbf{R}$, $|\mathbf{C}| > 1$, is said to be a circular wait (CW), if for every ordered pair $\{\mathbf{r}_i, \mathbf{r}_j\} \subset \mathbf{C}$, one has $\mathbf{r}_i \rightarrow \mathbf{r}_j$. Note that this is a generalization of the 'simple circuit' of [3] and [15], allowing the handling of more complex structures involving part-reentrance and resource pools. A CW \mathbf{C} is said to be in CB if (a) $m(\mathbf{C}) = 0$; and (b) for each $\mathbf{r} \in \mathbf{C}$, $\forall \mathbf{p} \in \mathbf{J}(\mathbf{r})$ with $m(\mathbf{p}) \neq 0$, $\mathbf{p}^* \in \mathbf{C}^*$.

The following is a structural analysis of deadlock for class MRF_1 systems in terms CWs in CB. The proofs for the novel results are given in the appendix, and others may be discovered in [10].

Theorem 1: Given a system of class MRF₁ with $\{N, m_0\}$, and marking $m \in \Re(m_0)$, the following are equivalent: (i) there exists a job-enabled dead transition at m; (ii) there exists a dead resource place at m; (iii) there exists CB at m.

The theorem establishes the equivalence between part-path deadlock and CB, and hence the marking at which this occurs. This lays the ground for the characterisation of deadlock as a FPN state. A useful corollary to the theorem is as follows.

Corollary 1: Given $\{N, m_0\}$, any $m \in \Re(m_0)$ is non-dead if there is no CB at m.

Thus, if there is no CB at $m \in \Re(m_0)$, then there is at least one enabled transition. The proof of one of the main results to be stated later, namely Theorem 3, depends on this result. Now, the occurrence of CB is closely related to the special FPN structures of *critical siphon* and *critical trap*. A critical siphon (trap) is a minimal siphon (trap) that does not contain any resource loop. Note that the critical siphon defined here is similar to the siphon considered in [4], which however fails to exploit the relation between the siphons and CWs when formulating its deadlock-prevention control policies.

For constructing critical siphons and traps, consider a CW C, and transition sets, $T_C^+={}^{\bullet}C \setminus C^{\bullet}$ and $T_C^-=C^{\bullet} \cdot C$. Define the *siphon-job set* as $J_s(C)=J(C) \cap {}^{\bullet}T_C^+$ and the *trap-job set* as $J_Q(C)=J(C) \cap {}^{T_C^{\bullet}}$. Then the sets $S_C = C \cup J_s(C)$ and $Q_C = C \cup J_Q(C)$ respectively define the critical siphon and critical trap associated with C. One can now state the following for $\{N, m_0\}$ of a MRF₁ system [10].

Theorem 2: A CW C in N is in CB at any $m \in \Re(m_0)$ if and only if $m(S_C)=0$.

This result shows that CB, and hence part-path deadlock, is equivalent to some marking (obviously not unique) that empties a specific critical siphon, namely the one associated with the CW that is in CB. Now, in keeping track of the marking of each critical siphon S_C, it is useful to regard each CW C as a token distribution centre, with m(C) defined as its kanban content, and partition the corresponding set of token receiving places J(C) as follows. First, note that in general $J_s(C) \cap J_o(C) = J_{so}(C) \neq \emptyset$, and define $\hat{J}_{s}(C) = J_{s}(C) \setminus J_{so}(C)$ and $\hat{J}_{o}(C) = J_{o}(C) \setminus J_{so}(C)$ respectively as the strictly siphon-job and strictly *trap-job sets*, and $J_N(C) = J(C) \setminus (J_S(C) \cup J_O(C))$ as the *neutral-job set*. The *critical subsystem* $J_O(C)$ associated with CW C, as defined in [10], can be then written as $J_o(C) = \hat{J}_Q(C) \cup J_N(C)$, and one has $J(C) = J_N(C) \cup J_{SO}(C) \cup \hat{J}_S(C) \cup \hat{J}_O(C)$, or $J(C) = J_S(C) \cup J_O(C)$, where the sets on the right hand-side in both equations are disjoint. Since $C \cup J(C) = \bigcup_{r \in C} L(r)$, at any $m \in \Re(m_0)$ one has $m_0(C) = m(C) + m(J(C)) = m(C) + m(J_s(C)) + m(J_o(C))$. Therefore $m(S_c) = 0$ if and only if $m(J_{0}(C))=m_{0}(C)$; or equivalently, $m(S_{C}) \neq 0$ if and only if $m(J_{0}(C)) \leq m_{0}(C)$, i.e., the work-inprocess in the critical subsystem $J_{0}(C)$ is limited above by $m_{0}(C)$ -1. This can be done by controlling an appropriate subset of $T(C) = C^{\bullet} = J(C)$, i.e., the transitions that distribute the tokens in C.

Now, one can write $T(C) = T_{pre}(C) \cup T_{pos}(C) \cup T_{N}(C)$, where the subsets are disjoint with $T_{pre}(C) = J_{o}(C) \setminus J_{o}(C)^{\bullet}$, $T_{pos}(C) = J_{o}(C)^{\bullet} \setminus J_{o}(C)$. Note also that $T_{pre}(C) = S_{C} \setminus S_{C} = \hat{J}_{Q}(C)$, $T_{pos}(C) = J_{o}(C)^{\bullet} \setminus J_{o}(C)$. Note also that $T_{pre}(C) = S_{C} \setminus S_{C} = \hat{J}_{Q}(C)$, $T_{pos}(C) = \hat{J}_{S}(C)$, and $T_{N}(C) = J_{N}(C)$, so that firing any $t \in T_{pre}(C)$, $t \in T_{pos}(C)$ and $t \in T_{N}(C)$

respectively reduces by 1, increases by 1, and leaves unaltered the token load of S_c . This observation is significant in devising deadlock-free job-dispatching policies.

Another structure in *N*, which is relevant in deadlock analysis, is the result of a *cyclic wait relation* between two CWs, defined below.

Definition 5: [Cyclic circular wait and key resource] Let $\{C_i, C_j\}$ be such that $|C_i \cap C_j|=1$, and define $C_i \cap C_j = \{r_b\}$. If $T_{pos}(C_i) \cap T_{pre}(C_j) \subset r_b^{\bullet}$ and $T_{pos}(C_j) \cap T_{pre}(C_i) \subset r_b^{\bullet}$, then $\{C_i, C_j\}$ is said to be a *cyclic CW* (CCW). If in addition $m_0(r_b) = 1$, then r_b is called a *key resource*.

As shown below, it is expedient to separate systems with key resources from those without. So let $\{N, m_0\}$ be called *regular* if it contains no key resources, and *irregular* otherwise. Now, define $M_F = \{m \in \Re(m_0) | m(S_C) = 0, \text{ for some CW C }\}$, i.e., the subset of reachable markings at which some CW C is in CB. Stated below is a fundamental result for regular $\{N, m_0\}$.

Theorem 3: [*Main Theorem*] If $\{N, m_0\}$ is regular, then at every $m \in \Re(m_0) \setminus M_F$, there exists at least one transition that is enabled to fire without resulting in CB.

This result says that given $\{N, m_0\}$ with no key resources, if at any marking reached there is no CB, then there always exists at least one transition that is enabled to fire without causing CB. This means that part-flow will continue provided CB can be avoided. This leads to the next fundamental result, which says that for regular systems, system deadlock is equivalent to part-path deadlock and hence to CB.

Theorem 4: [Main Theorem] Given $\{N, m_0\}$ regular, N has system deadlock at any $m \in \mathfrak{R}(m_0)$ if and only if $m \in M_F$.

Therefore for regular systems avoiding CB is a necessary and sufficient condition for avoiding system deadlock, and hence guaranteeing that the processing of every part is completed. This result generalizes a similar but much more limited one offered in [3] and [8], whereby the flowline systems under study have neither any resource pools (i.e. $m_0(r)=1$, all $r \in \mathbb{R}$) nor any reentrance. It must be noted that when $\{N, m_0\}$ has key resources, the system may run into system deadlock several steps

before any CB actually occurs. A characterisation of system deadlock for this more complex case of irregular systems can be obtained by using the structural framework that is presented in [10] and developed herein (more on this in future work).

5. Dispatching With Deadlock Avoidance

According to Theorems 2 and 4, deadlock-free dispatching for regular MRF₁ system requires that no critical siphon ever becomes empty. The dynamic deadlock-free dispatching strategy presented below defines a *family of generalized kanban dispatching rules*. It is a one-step-look-ahead control policy, and is maximally permissive, also maximizing the percent utilisation of resources. It is capable of dealing with the complexities arising from the 'interleaved' configuration of the MRF systems noted in [11].

Assuming that jobs are dispatched singly, the notion of *dispatching priority* is defined as follows. Given two activated (i.e., all the preconditions for their execution satisfied) jobs J_i^k and J_j^l , J_i^k is said to be dispatched with priority over J_j^l , denoted $pri(J_i^k) > pri(J_j^l)$, if whenever both jobs are requested simultaneously J_i^k is given the preference. Given two job sets S_1 and S_2 , $pri(S_1) > pri(S_2)$ implies $pri(J_i^k) > pri(J_j^l)$ for every $J_i^k \in S_1$ and $J_j^l \in S_2$. The foregoing analysis leads to a multi-part LBFS dispatching rule that is deadlock-free, more general than the uniform LBFS policy of [11], and is easy to implement

Theorem 5: Given $\{N, m_0\}$ regular, suppose a LBFS dispatching policy is used such that, at any m whenever a multitude of jobs $\{J_i^k\}$ are activated simultaneously, they are dispatched according to the following: for every CW C such that $\{J_i^k\} \cap J(C) \neq \emptyset$: (i) set $pri(J_s(C) \cup J_N(C)) > pri(\hat{J}_Q(C))$, and (ii) do not dispatch any $J_i^k \in \hat{J}_Q(C)$ if $m(\hat{J}_Q(C) + m(J_N(C)) = m_0(C) - 1$. Then, deadlock will not occur.

The deadlock-free dispatching policy stated next defines the generalized kanban strategy.

Theorem 6: Given $\{N, m_0\}$ regular, any dispatching policy U is deadlock-free if and only if, for all $m \in M_U$, when assigning dispatching priorities to a multitude of simultaneously activated jobs $\{J_i^k\}$, it

disallows dispatching any J_i^k whenever there is some CW C such that $J_i^k \in \hat{J}_Q(C)$ and $m(\hat{J}_Q(C) + m(\hat{J}_N(C)) = m_0(C) - 1.$

The proofs for both of these results follow directly from Definition 4 and Theorem 4. The 'if and only if' condition of Theorem 6 shows that the control strategy is maximally permissive. Moreover, by keeping the kanban content m(C) as low as possible (including zero), the WIP in the critical subsystem $J_0(C)$ is maximized, thus maximizing the percent utilisation of resources. The difference between LBFS rule of Theorem 5 and the generalized kanban strategy of Theorem 6 is that LBFS rule is applied at all times, while the kanban rule is applied for a given CW C only when m(C)=1. In the latter case, there exists a possibility of loading the critical subsystem (cf. FBFS rule) even until m(C)=0. This is not possible for the LBFS case, for which the percent resource utilization depends on the 'luck of the draw' in terms of arrival rates and actual transition firing times. Note that the deadlock-free aspect in both of the proposed policies is based only on the structural properties of the system and is therefore valid independently of the specific part arrival times and job duration/resource set-up times.

6. Matrix Computations For Dispatching Policy Implementation

Though PNs are extremely useful in modeling and analysis of manufacturing system behaviour, they do not generally lend themselves to computationally tractable algorithms. This section attempts to remedy this drawback by providing matrix techniques to determine all the aforementioned structures, which is essential for the implementation of the proposed dispatching policies. A more detailed foundation for what follows may be discovered in [10].

Given *N*, define the composite binary vector $\mathbf{p}^{T} = [\mathbf{v}^{T} \ \mathbf{r}^{T}]$, where v and r correspond to job and resource places, J and R, respectively. Then, the PN incidence matrix can be accordingly partitioned as $W = O - I = S^{T} - F = [S_{v}^{T} - F_{v} \ S_{r}^{T} - F_{r}]$, where $S^{T} = O$ and F = I. The sub-matrices S_{v}^{T} , S_{r}^{T} are the output incidence matrices, and F_{v} , F_{r} are the input incidence matrices associated with the job and resource places, respectively.

To implement the policies of Theorems 5 and 6, all the CWs in N and the associated job subsets, $J_{s}(C)$, $\hat{J}_{Q}(C)$ and $J_{N}(C)$ must be determined. In [10] an algorithm is given that yields the complete set $\{C_i\}$ of CWs, represented by a binary matrix X, with its *i*-th column χ_i corresponding to CW C_i . The algorithm is based on the wait relation graph G_w and its adjacency matrix G_w , which can be computed as $G_w = S_r F_r$, where the matrix multiplication is in and/or algebra, and S_r and F_r are as above. For each vector χ_i , define an equal size vector χ_{is} as its projection onto the set of shared resources, i.e., χ_{is} has nonzero entries only for the *shared* resources contained in CW C_i . Then, the job subsets $J_s(C_i)$ and $J_Q(C_i)$ are given by the vectors $J_S(C_i) = F_v^T[S_r^T \chi_{is} \wedge \overline{F_r \chi_i}]$, and $J_Q(C_i) = F_v^T[F_r \chi_{is} \wedge \overline{S_r^T \chi_i}]$. One can use these to determine the other two job subsets $\hat{J}_Q(C_i)$ and $J_N(C_i)$ from $\hat{J}_Q(C_i) = J_Q(C_i) \wedge \overline{J_S(C_i)}$, and $J_N(C_i) = F_v^T S_r^T c_i \wedge \overline{J_Q(C_i) \vee J_S(C_i)}$. In all of these equations the matrix operations are carried out in and/or algebra, and \land and \lor respectively denote the element-by-element matrix 'and' and 'or' operations. A proof for the result for $J_s(C)$ is presented in [10]. The other three stated herein can easily be proved using a similar approach.

It is important to note that for a given system configuration this structural analysis is performed offline and only once. Moreover, all the computations involved depend on matrix operations so that they are of polynomial complexity.

7. Conclusion

The deadlock problem in a class of finite-buffer MRF systems is discussed. The notions of part-path deadlock and system deadlock are introduced and linked to the notion of circular blocking and the related PN structures of circular waits, critical siphons and traps. For a general class of finite-buffer MRF systems without assembly, a necessary and sufficient condition for system deadlock is provided. This leads to a maximally permissive one-step-look-ahead deadlock-free dispatching control strategy. The result is a generalized kanban class of dynamic dispatching policy, which is more general than the multiclass LBFS policy of [11] and is guaranteed to not only avoid system deadlock but also provide space for

maximum resource utilization. Computationally efficient matrix techniques are given for determining the structures that are required for implementing the proposed dispatching schemes. Future research will focus on applying the proposed formalism to a discrete-event simulator popular with industrial engineering applications, as well as extending the framework to deal with more complicated structures.

References

- [1] Z.A. Banaszak and B.H. Krogh, "Deadlock avoidance in flexible manufacturing systems with concurrently competing process flows," *IEEE Trans. Robotics and Automation*, vol. 6, no. 6, pp. 724-734, Dec. 1990.
- [2] Buzacott and D.D. Yao, "Flexible manufacturing systems: a review of analytical models," *Management Sci*, vol. 32, no. 7, pp. 890-905, July 1986
- [3] H. Cho, T.K. Kumaran, and R.A. Wysk, "Graph-theoretic deadlock detection and resolution for flexible manufacturing systems," *IEEE Trans. Robotics and Automation*, vol. 11, no. 3, pp. 413-421, 1995.
- [4] J. Ezpeleta, J.M. Colom, and J. Martinez, "A Petri net based deadlock prevention policy for flexible manufacturing systems," *IEEE Trans. Robotics and Automation*, vol. 11, no. 2, pp. 173-184, 1995.
- [5] L. E. Holloway and B. H. Krogh, "On closed-loop liveness of discrete-event systems under maximally permissive control," *IEEE Trans. Automat. Control*, vol. 37, no. 5, pp. 692-697, 1992.
- [6] F.S. Hsieh and S.C. Chang, "Dispatching-driven deadlock avoidance controller synthesis for flexible manufacturing systems," *IEEE Trans. Robotics and Automation*, vol. 10, no 2, pp. 317-327, April 1994.
- [7] M.D. Jeng and F. DiCesare, "Synthesis using resource control nets for modeling shared-resource systems," *IEEE Trans. Robotics and Automation*, vol. 11, no 3, pp. 317-327, 1995.
- [8] T.K. Kumaran, W. Chang, N. Cho, R.A. Wysk, "A structured approach to deadlock detection, avoidance, and resolution in flexible manufacturing systems," *Int. J. Prod. Res.*, vol. 32, no. 10, pp. 2361-2379, 1994.
- [9] P.R. Kumar and S.P. Meyn, "Stability of queueing networks and scheduling policies," *IEEE Trans. Automat. Control*, vol. 40, no. 2, pp. 251-260, 1995.
- [10] F.L. Lewis, A. Gürel, S. Bogdan, A. Doğanalp and O.C. Pastravanu, "Analysis of deadlock and circular waits using a matrix model for discrete event manufacturing systems," *Automatica*, 1998.
- [11] S.H. Lu and P.R. Kumar, "Distributed scheduling based on due dates and buffer priorities," *IEEE Trans. Automat. Control*, vol. 36, no. 12, pp.1406-1416, Dec. 1991.
- [12] T. Murata, "Petri nets: properties, analysis and applications," *Proc. IEEE*, vol. 77, no. 4, pp. 541-580, 1989.
- [13] S.S. Panwalker and W. Iskander, "A survey of scheduling rules," *Operations Research*, vol. 26, no. 1, pp. 45-61, 1977.
- [14] T.I. Seidman, "'First come first serve' is unstable!," Univ. Maryland Baltimore County, Tech. Report, 1993.
- [15] K.Y. Xing, B.S. Hu and H.X Chen, "Deadlock avoidance policy for Petri-net modelling of flexible manufacturing systems with shared resources," *IEEE Trans. Automat. Control*, vol. 41, no. 2, pp. 289-295, 1991.
- [16] M.C. Zhou, F. DiCesare, A.D. Desrochers, "A hybrid methodology for synthesis of Petri net models for manufacturing systems," *IEEE Trans. Robotics and Automation*, vol. 8, no. 3, pp. 350-361, Jun. 1992.