

# Analysis of Transient Recovery Voltage in 400 kV SF<sub>6</sub> Circuit Breaker Due to Transmission Line Faults

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**Abstract** – Transient recovery voltage (TRV) is calculated according to the IEC standards and using the EMTP-ATP software. Modeling of electric arc for the SF<sub>6</sub> circuit breaker is presented. Influence of grading capacitors and line bay disposition on the TRV waveform on the side of the line in a 400 kV substation is analyzed for the short-line fault (SLF) conditions. Fault location at which the rate of rise of recovery voltage (RRRV) is the highest was determined using the EMTP-ATP simulations. Results obtained according to the IEC standards and using the EMTP-ATP are compared. **Copyright** © 2011 Praise Worthy Prize S.r.l. - All rights reserved.

**Keywords:** Transient Recovery Voltage, Short-Line Fault, SF<sub>6</sub> Circuit Breaker, Electric Arc Model, EMTP Simulations

## Nomenclature

$v$	Arc voltage
$i$	Arc current
$g$	Arc conductance
$P(g)$	Arc cooling power
$T(g)$	Arc thermal constant
$g_0$	Initial arc conductance
$\alpha, \beta$	Arc constants
$\Delta t$	Simulation time step

## I. Introduction

TRV is the voltage across the opening contacts of a fault-interrupting circuit breaker (CB) immediately after the electric arc is extinguished [1]. This voltage may be considered in two successive time intervals: one during which the transient voltage exists, followed by the second one during which the power frequency voltage exists alone.

For TRV studies, the two most important factors are: the maximum voltage attained depending on the normal system operating voltage and the RRRV during oscillation, which is also dependent on the frequency of oscillations [2]-[5]. The impact of the line bay disposition on the TRV waveform on the side of the line in a 400 kV substation is analyzed in the SLF conditions. A comparison of results obtained from calculations conducted according to the IEC standards [6] and using the EMTP-ATP software is done.

The influence of various parameters on TRV is analyzed: stray capacitances of the voltage instrument transformers; distance from the fault location to the CB; capacitors in parallel with CB poles; electric arc modeling.

## II. Short-Line Fault

Occurrence of a fault on the overhead line relatively close to a CB is characterized by the TRV oscillation from the line side. This oscillation is caused by a short length of the line between the CB and the place of the fault. In the transitional period of establishing TRV on the line side to the first peak, which lasts several microseconds, there is a danger of occurrence of thermal breakdown in the SF<sub>6</sub> CB. The IEC standards define the procedure for the determination of TRV on the line side when the CB is tested in SLF conditions.

Information about TRV that a CB is expected to encounter in service is of great importance in its design and operation. Triangular-shaped TRVs are associated with SLF after current interruption and the line side voltage exhibits the characteristic triangular waveform. The rate of rise of the saw-tooth shaped TRV is a function of the line surge impedance and it is generally higher than that experienced with exponential or oscillatory TRVs. However, the TRV peak is generally lower. Because overhead lines have distributed electrical parameters (series resistance and inductance and shunt conductance and capacitance), the line side voltage oscillates in the form of a travelling wave with positive and negative reflections at the open breaker and at the fault location, respectively. Fig. 1 shows the line side component of TRV, which has a saw-tooth shape and the recovery voltage on the source side that rises much more slowly. Only the line side triangular recovery voltage is important during the early portion of the TRV [7].

The closer the fault to the CB, the higher the initial rate of rise of the line side recovery voltage due to the higher fault current, while the crest magnitude of this line side triangular wave decreases due to the shorter time needed for the reflected wave to return. The

amplitude and the rate of rise of TRV for these short-line faults are determined on a single-phase basis during their early time periods (typically less than 20 μs), when the source side voltage changes only slightly.

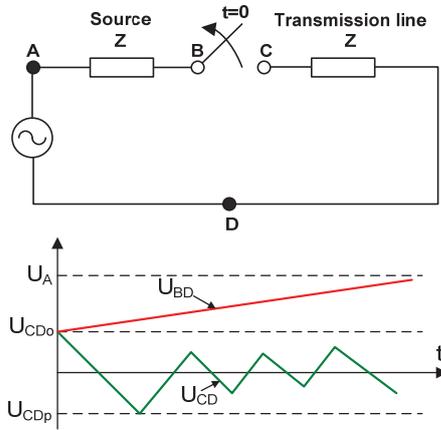


Fig. 1. TRV waveform for SLF ( $U_{BD}$  – source side voltage,  $U_{CD}$  – line side voltage)

### III. Electric Arc Model for SF<sub>6</sub> Circuit Breaker

For calculation of the initial part of TRV, the modeling of the arc resistance in a SF<sub>6</sub> CB is important, because it has a significant impact on the TRV. Black box models use a mathematical description of the electrical behavior of an electrical arc. These types of models do not give full representation of the physical processes taking place inside the CB [8]. Recorded voltage and current traces during the "thermal period" are used to obtain the CB parameters that are later on substituted in differential equations. The most basic Cassie-Mayr equation is often used, as well as its modifications from the Schwarz-Avdonin model (1):

$$\frac{1}{g} \cdot \frac{dg}{dt} = \frac{1}{T(g)} \cdot \left[ \frac{u \cdot i}{P(g)} - 1 \right] \quad (1)$$

Calculation of the arc conductance requires data on the cooling power and the thermal time constant. Parameters  $P(g)$  and  $T(g)$  are conductance dependent. The cooling power  $P$  and the thermal time constant  $T$  depend on temperature, CB type and design, and they can be defined as a function of conductance (2), (3):

$$T(g) = T_0 \cdot g^\alpha = 1.5\mu s \cdot g^{0.17} \quad (2)$$

$$P(g) = P_0 \cdot g^\beta = 4MW \cdot g^{0.68} \quad (3)$$

where  $\alpha$  and  $\beta$  are constants whose values are given in Fig. 2. Arc equation could be solved if a sufficiently small time interval ( $\Delta t$ ) can be observed in which  $P$  and  $T$  are constant. The differential equation (1) was solved

numerically with the Euler method for differential equations of the first order (4):

$$g(t + \Delta t) = g(t) + \left( \frac{i^2}{P_0 \cdot g(t)^\beta} - g(t) \right) \cdot \left( 1 - e^{-\frac{\Delta t}{T(g)}} \right) \quad (4)$$

Depending on the difference between input (power) and cooling thermal energy (power loss), the arc temperature (and conductivity) is either going to increase or decrease. The test circuit described in [9] is shown in Fig. 2. The first step in solving equation (4) is to find solutions to the network equations (in EMTP) with the initial conductance value  $g_0 = g(t_0)$ . The solution to these equations gives the value for the current  $i(t_0)$ .

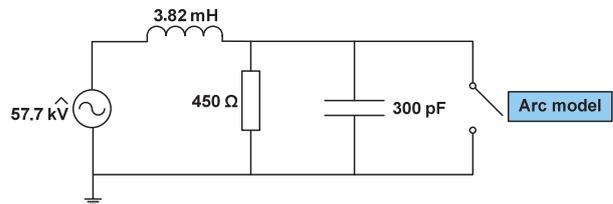


Fig. 2. Test circuit and arc model parameters:  $P_0=4$  MW and  $\beta=0.68$ ;  $T_0=1.5$  μs and  $\alpha=0.17$  [9]

The initial values of  $P(g_0)$  and  $T(g_0)$  are also calculated for the conductance initial value. The low initial value of arc resistance at the time step  $t_0$  was assumed and the conductance in the next time step was calculated from equation (4) in MODEL section of the ATP-EMTP. The output value from MODEL section is resistance and it is used as input value for nonlinear resistance component R(TACS) Type 91. A time step in ATP-EMTP has to be fixed and in some cases the selection of a very small time step leads to numerical problems and can prolong computation time. The calculated post zero arc current and peak value of the arc voltage were compared to three different models described in [9].

The comparison of results is shown in Figs. 3-6.

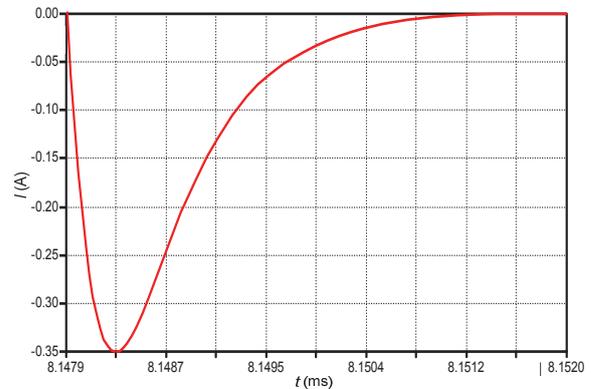


Fig. 3. Calculated post arc current  $I_{\max}=-0.35$  A

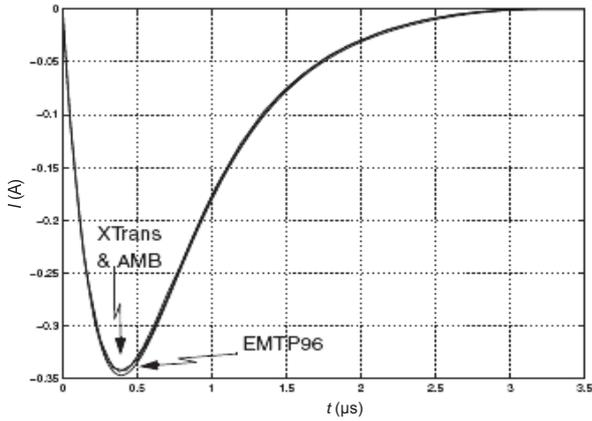


Fig. 4. Post arc current – comparison with reference models [9]

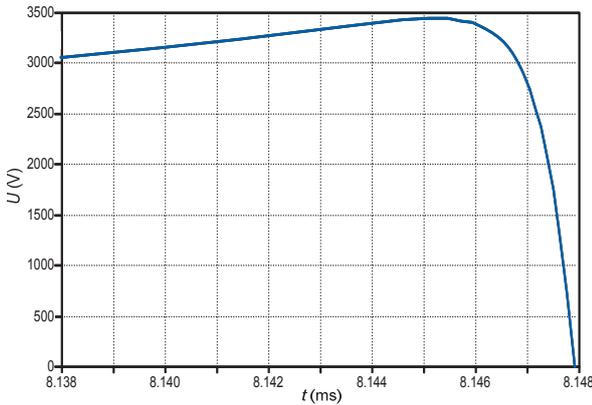


Fig. 5. Calculated arc voltage  $U_{max}=3442$  V

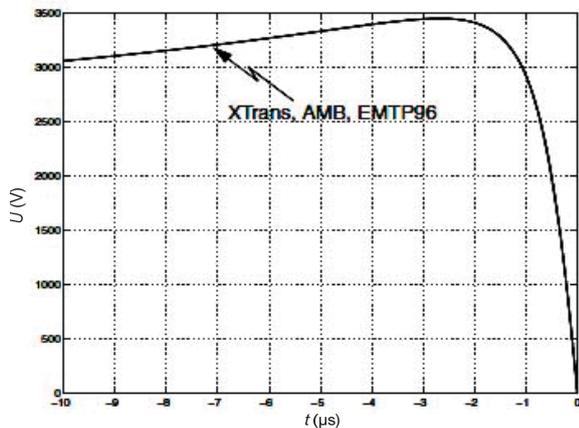


Fig. 6. Arc voltage – comparison with reference models [9]

#### IV. Modeling of Substation and Transmission Line

The model of high frequency reactors that are installed in two phases (A and B) is depicted in Fig. 7. The capacitor voltage transformer in line bay compensates the high frequency reactor used for communication. The whole substation and transmission line depicted in Fig. 8 were modeled in detail (Fig. 9).

On the line side of the CB following devices were modeled with their stray capacitances: current transformer (CT) 680 pF; capacitor voltage transformer (CVT) 4400 pF; inductive voltage transformer 550 pF; and disconnector 200 pF [10].

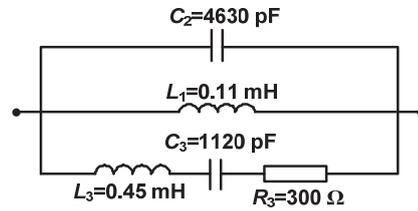


Fig. 7. High frequency reactor model

On the source side of the CB the main busbar system was modeled with LCC-JMartí model [11], and post insulators with capacitances to the ground. The equivalent network, which consisted of two line bays and two transformer bays, was represented by a voltage source and a short-circuit impedances.

#### V. Calculation of SLF According to IEC

A 400 kV CB with the rated short-circuit breaking current of 40 kA<sub>rms</sub> should be able to interrupt the short-circuit current of 36 kA<sub>rms</sub> in the cycle L90 (90% of the rated short-circuit breaking current) if subjected to the TRV, as it is proscribed by the IEC, with the rate of rise of about 7.2 kV/μs.

This rate of rise can be easily calculated using analytical expressions given in [6]. The short-circuit current of 40 kA in the substation was determined by adjusting the equivalent network on the source side of the CB. Short circuit calculations were performed at different distances from the substation in order to determine the RRRV at the current of 36 kA.

The "critical distance" of a fault location from the substation in the cycle L90 was used for the testing of interrupting capability of CB. Fault location at which RRRV is the highest was determined using the ATP simulations. A total of 12 simulations of single-phase SLF in phase A at various distances from the substation were conducted. The short circuit occurs at the time instant  $t=0$  s, when the voltage in phase A has the maximum value.

For SFL at the "critical distance" of 1000 m from the substation (short-circuit current 36 kA), the RRRV on the line side and the CB equal 5.8085 kV/μs and 5.5810 kV/μs, respectively. The RRRV computed with EMT96 differs from the one obtained by IEC due to detailed modeling of the substation. If the fault distance is greater than 2000 m, the RRRV and the short-circuit current decrease.

Also, for all distances shorter than 2000 m RRRV decreases, and short-circuit current increases (Figs. 10 and 11).

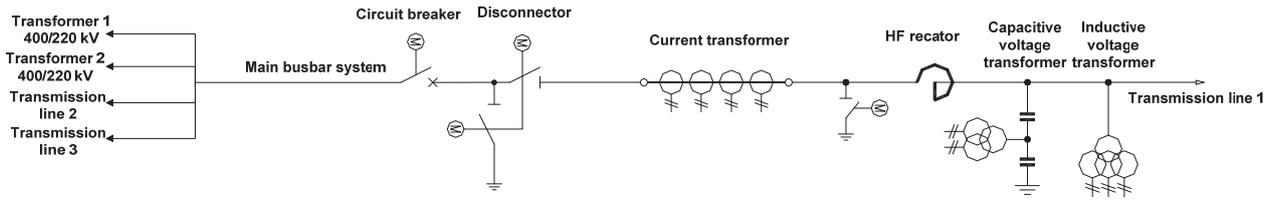


Fig. 8. 400 kV line bay

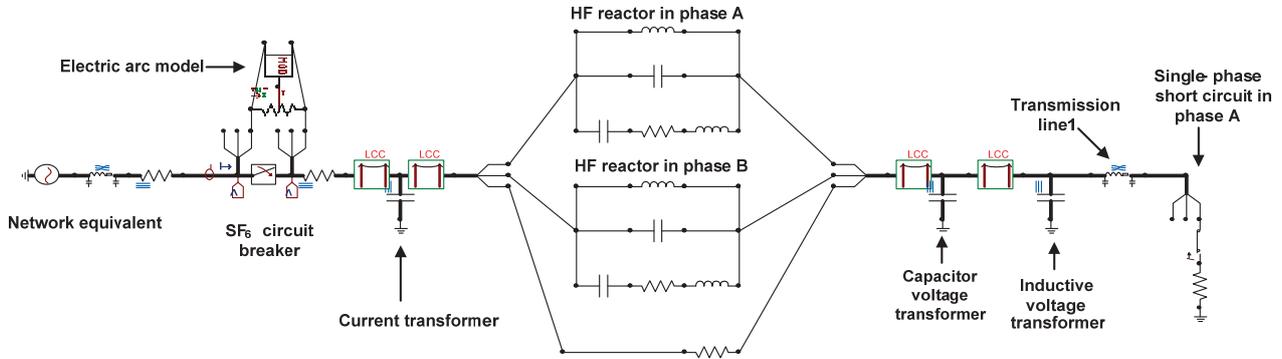


Fig. 9. Three-phase model for TRV calculation of SLF according to IEC

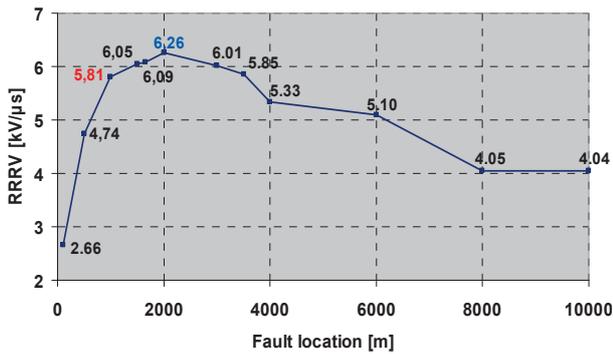


Fig. 10. RRRV value versus distance of SLF

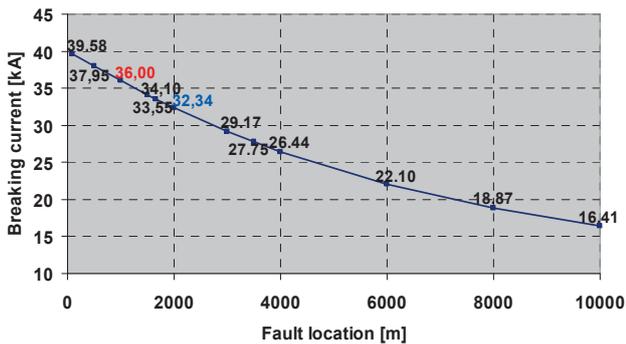


Fig. 11. Breaking current versus distance of SLF

The maximum RRRV on the line side is 6.0702 kV/μs if the SLF occurs at the distance of 2000 m (short-circuit current 32.34 kA); as shown in Fig. 12. Fig. 13 shows the transient voltage on the CB with the maximum value of  $U_{max} = -515$  kV.

## VI. Calculation of SLF in an actual system

The calculation of SLF was conducted in order to obtain RRRV values in real operating conditions and the full operating state was assumed. For SLF at the distance of 1000 m with the fault current of 8.76 kA<sub>rms</sub> (Fig. 15), the RRRV on the line side and on the CB equal 1.3418 kV/μs and 1.3620 kV/μs (Fig. 14), respectively.

Simulations were performed for different SLF locations. The maximum values of RRRV were found for the fault current of 8.43 kA<sub>rms</sub> at the distance of 2000 m. In the real operating conditions (Fig. 18) the SLF currents exhibit values lower than the rated short-circuit breaking current. The RRRV on the line side and the CB were 1.6943 kV/μs (Fig. 16) and 1.7618 kV/μs, respectively. In this case TRV on the CB is  $U_{max} = 670$  kV (Fig. 17).

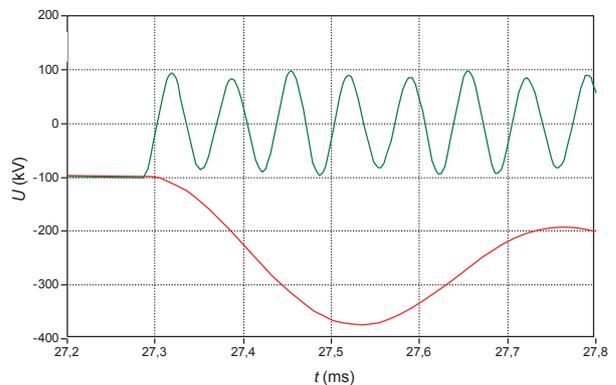


Fig. 12. TRV on the line side RRRV=6.0702 kV/μs (green) and on the source side (red)

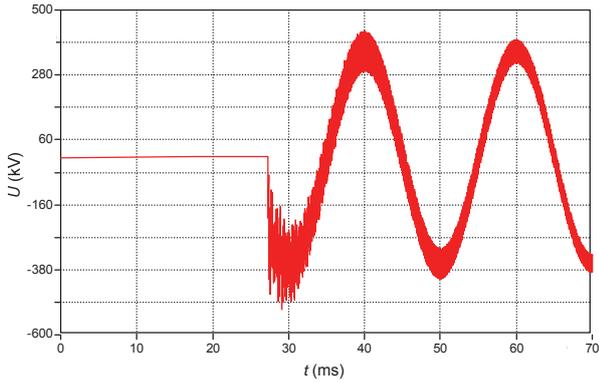


Fig. 13. TRV for 32.34 kA on CB;  $U_{max}=-515$  kV

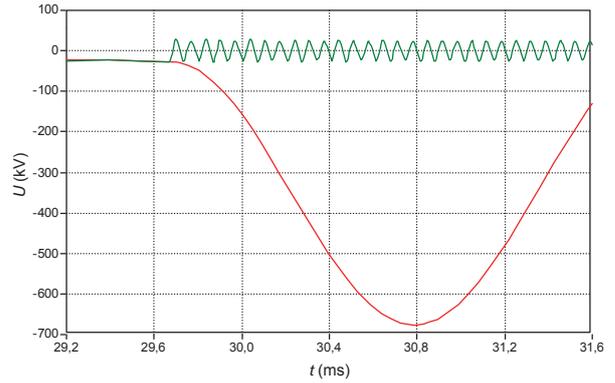


Fig. 16. TRV on the line side (green) RRRV=1.6943 kV/μs and on the source side (red)

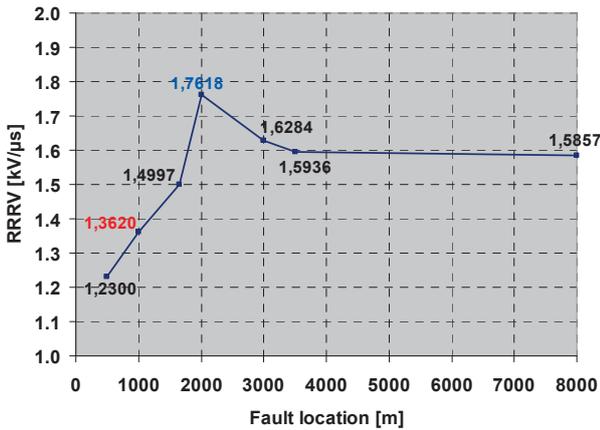


Fig. 14. RRRV value versus distance of SLF

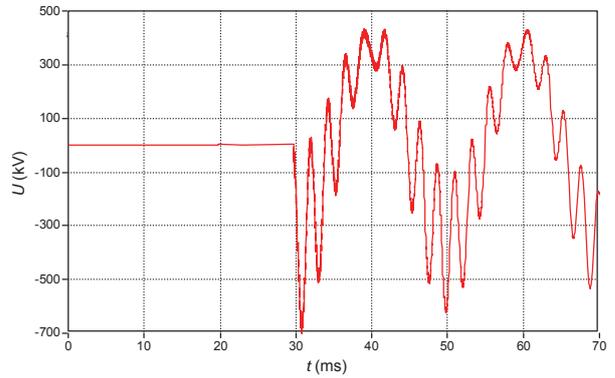


Fig. 17. TRV for 8.43 kA on CB;  $U_{max}=-670$  kV

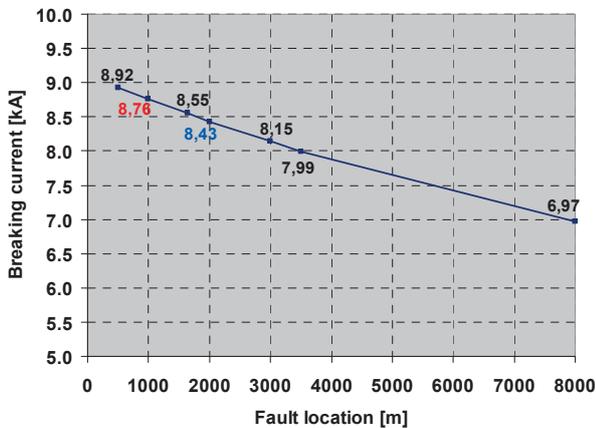


Fig. 15. Breaking current versus distance of SLF

## VII. Influence of the Distance of CVT from CB on TRV Steepness

The impact on the RRRV of the distance between the CB and the CVT with stray capacitance of 4400 pF was investigated. In reality, the CVT is located approximately 37 m from the CB and the SFL at the distance of 1000 m was analyzed. The results of the RRRVs for various distances are shown in Fig. 19.

From the results of these simulations it can be concluded that the RRRV on CB reduces with smaller distances between the CB and CVT. At the distance of 30 m the RRRV is minimal and it increases with the further reduction of distance.

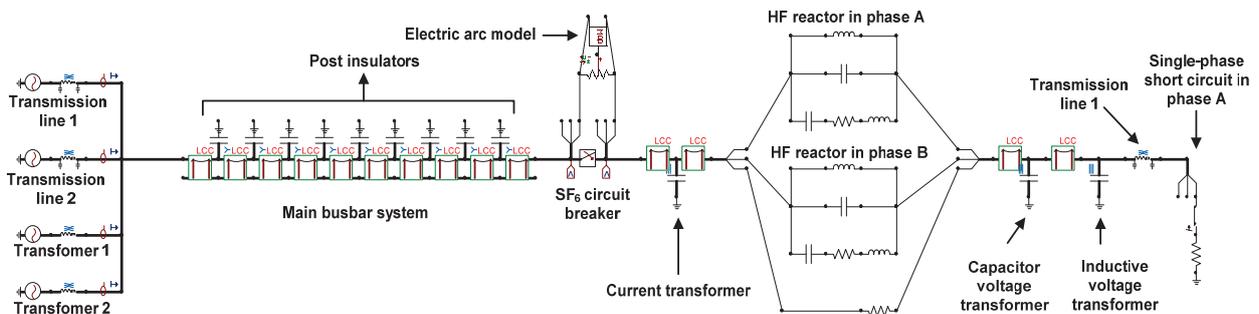


Fig. 18. Three-phase model of an actual system for TRV calculation in conditions of SLF

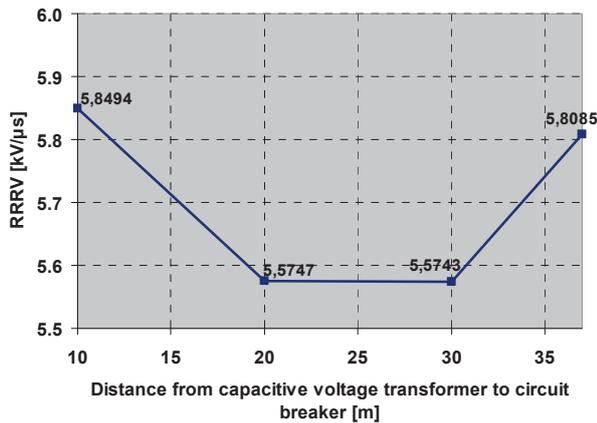


Fig. 19. RRRV versus distance between inductive voltage transformer and CB

### VIII. Influence of Grading Capacitors on TRV

Modern SF<sub>6</sub> CBs are equipped with capacitors connected in parallel to CB terminals in order to equalize the voltage distribution and to decrease the RRRV on the CB contacts. Their influence was analyzed in the case of SLF at the distance of 2000 m from the substation for the fault current of 32.34 kA<sub>eff</sub>. The results of these calculations are shown in Fig. 20.

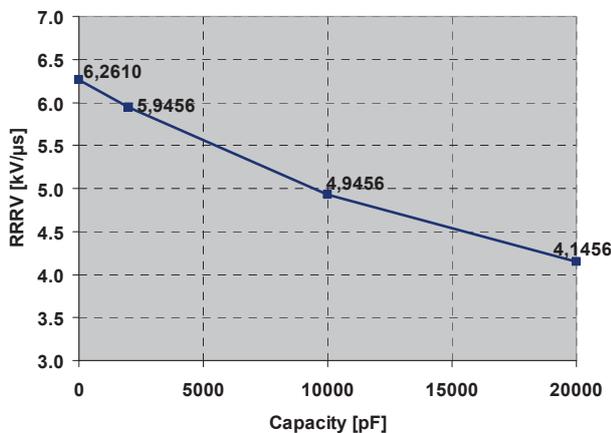


Fig. 20. RRRV versus capacity of grading capacitors

### IX. Conclusion

TRV is calculated using EMTP-ATP software and a comparison of results obtained according to IEC procedure is performed.

The modeling of the arc resistance in a SF<sub>6</sub> CB is important, because it has a significant impact on the initial part of TRV. The Schwarz-Avdonin model was implemented in ATP-EMTP for a mathematical description of the electrical behavior of an electrical arc.

A 400 kV CB with the rated short-circuit breaking current of 40 kA<sub>rms</sub> should be able to interrupt the short-circuit current of 36 kA<sub>rms</sub> in the cycle L90 if subjected

to the TRV, as it is proscribed by the IEC, with the rate of rise of about 7.2 kV/μs.

According to conducted computer simulations for the short-circuit current of 36 kA<sub>rms</sub> rate of rise on CB was 5.81 kV/μs at the fault distance of 1000 m. The RRRV computed with ATP differs from the one obtained by IEC procedure due to the detailed modeling of the substation and the electric arc.

The fault location at which the rate of rise is the highest was determined using the EMTP-ATP simulations. The maximum calculated rate of rise was 6.26 kV/μs for the fault current of 32.34 kA at the distance of 2000 m. In the real operating conditions short-circuit currents are lower and maximum calculated rate of rise was 1.7618 kV/μs at the current of 8.43 kA and distance of 2000 m. RRRV on the CB reduces with the reduction of distances between the CB and the CVT up to 30 m and it increases with further reduction of distance.

The interrupting capability of the CB in SLF conditions is greatly increased with the additional capacitors installed in parallel to the CB terminals, due to the reduction of RRRV.

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